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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/766,536	01/27/2004	Ping-Chin Cheng	E0523-00011 (AU0303041)	3087
8933	7590	12/27/2005	EXAMINER	
DUANE MORRIS, LLP IP DEPARTMENT 30 SOUTH 17TH STREET PHILADELPHIA, PA 19103-4196			TON, MINH TOAN T	
			ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 12/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

11A

Office Action Summary	Application No.	Applicant(s)	
	10/766,536	CHENG ET AL.	
	Examiner	Art Unit	
	Toan Ton	2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-38 is/are pending in the application.
- 4a) Of the above claim(s) 17-22 is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-16 and 23-38 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

Election/Restriction

1. An election of Group I corresponding to claims 1-16 and 23-38 is acknowledged. Claims 17-22 are withdrawn from consideration.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-14, 16, 23-36 and 38 are rejected under 35 U.S.C. 102(b) as being anticipated by Kawasaki (US 6424400).

Kawasaki discloses a method of bonding integrated circuit chips and other devices to a liquid crystal display panel, the method comprising the steps of (see at least Figures 1-2): applying an anisotropic conductive film 21 to a region of the liquid crystal display panel, the film sized to bond a first integrated circuit chip (e.g., 3a/3b) and an other device (e.g., 13a/13b) to the panel; placing one of the first integrated circuit chip and the other device on a first area portion of the film; compressing the one of the first integrated circuit chip and the other device together with the panel.

Kawasaki discloses the method comprising the steps placing the other one of the first integrated circuit chip and the other device on a second area portion of the film, compressing the other one of the first integrated circuit chip and the other device together with the panel (see at least Figures 1-2).

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Kawasaki discloses the region of the panel comprises a peripheral region (see at least Figures 1-2).

Kawasaki discloses the first integrated circuit chip comprises a first driver integrated circuit chip (see at least Figures 1-2).

Kawasaki discloses the anisotropic conductive film spaced from/extends beyond an edge of the panel an edge of the panel (see at least Figures 1-2).

Kawasaki discloses the other device (e.g., 13) selected from the group consisting of a flexible printed circuit board, a tape carrier package, and a chip-on-film (see at least Figures 1-2).

Kawasaki discloses the method comprising the anisotropic conductive film sized to bond the first integrated circuit chip (e.g., 3a), a second integrated circuit chip (e.g., 3b) and the other device (e.g., 13a/13b) to the panel, the placing step includes placing one of the first integrated circuit chip, the second integrated chip and the other device on the first area portion of the film, and the compressing step includes compressing the one of the first integrated chip, the second integrated chip and the other device together with the panel (see at least Figures 1-2).

Kawasaki discloses the method comprising the steps of placing another one of the first integrated circuit chip (e.g., 3a), the second integrated circuit chip (e.g., 3b) and the other device (e.g., 13a/13b) on a second area portion of the film; compressing the another one of the first integrated circuit chip, the second integrated circuit chip and the other device together with the panel (see at least Figures 1-2).

Kawasaki discloses manufacturing steps including heating and curing the anisotropic conductive film (see at least col. 2, lines 35-37).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 15 and 37 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kawasaki (US 6424400) as applied to claims 1-14, 16, 23-36 and 38 above.

It is known and a common goal in the art to minimize/reduce manufacturing steps (e.g., simultaneously formed, i.e., formed at the same time) for advantages such as cost-reduction. Therefore, it would have been at least obvious to one having ordinary skill in the art at the time the invention was made to form the integrated circuit chip(s) and/or the other device simultaneously for advantages such as cost reduction.

Other manufacturing steps such as steps recited claims 15, 37 appear at least obvious variations (i.e., not patentably distinct) to manufacturing steps such as steps recited in the above claims. Therefore, it would have at least obvious to one having ordinary skill in the art at the time the invention was made to employ other manufacturing steps such as steps recited claims 15, 37 appear at least obvious variations (i.e., not patentably distinct) to manufacturing steps such as steps recited in the above claims.

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Conclusion


5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Contact Information

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

December 22, 2005


TOANTON
PRIMARY EXAMINER